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Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A liquid crystal display ~~including a plurality of pixel areas, each pixel area~~ comprising:

a pixel ~~area~~electrode enclosed ~~defined~~ by a first ~~transverse extending~~ gate line, a second ~~transverse extending~~ gate line, a first ~~lengthwise extending~~ data line, and a second ~~lengthwise extending~~ data line;

~~a pixel electrode formed overlying the pixel area;~~

a switching element positioned on the first gate line ~~electrically connected to the pixel electrode~~;

~~a thin film transistor positioned on one of the first or the second transverse extending gate lines, comprising a source electrode and a drain electrode; and~~

a first shielding layer ~~directly connected to~~ having an edge between the first ~~gate~~ data line and the pixel electrode; and, ~~wherein the first shielding layer is parallel to the first data line and adjacent to the first data line, and overlaps across the source electrode of the thin film transistor of the adjacent pixel area.~~

a second shielding layer having an edge between the second data line and the pixel electrode, wherein the width of the first shielding layer is larger than the width of the second shielding layer and the switching element is adjacent to the first shielding layer.

Claim 2 (currently amended): The ~~L~~liquid crystal display as claimed in claim 1, wherein the first shielding layer overlaps the periphery of the pixel electrode ~~to provide a first overlapping portion~~.

Claim 3 (currently amended): The liquid crystal display as claimed in claim 1, ~~further comprising a second shielding layer parallel to the second data line and adjacent to the~~

~~second data line.~~wherein the first shielding layer directly connects to the first gate line.

Claim 4 (currently amended): The liquid crystal display as claimed in claim ~~3~~1, wherein the second shielding layer directly connects ~~is not electrically connected~~ to the first gate line.

Claim 5 (currently amended): The liquid crystal display as claimed in claim ~~3~~1, wherein a spacing between the first data line and the ~~periphery of the~~ pixel electrode is a liquid crystal reverse region~~, and a spacing between the second data line and the periphery of the pixel electrode is a liquid crystal non-reverse region~~.

Claim 6 (currently amended): The liquid crystal display as claimed in claim ~~5~~1, wherein a spacing between the second data line and the pixel electrode is a liquid crystal non-reverse region. ~~the width of the first shielding layer adjacent to the liquid crystal reverse region is larger than the width of the second shielding layer adjacent to the liquid crystal non-reverse region.~~

Claim 7 (currently amended): The liquid crystal display as claimed in claim ~~3~~1, further comprising a repair line situated across the first shielding layer and the second shielding layer~~, wherein (i) the repair line partially overlaps the first shielding layer in order to provide a first repair point, and (ii) the repair line partially overlaps the second shielding layer to provide a second repair point~~.

Claims 8-28: (canceled)

Claim 29 (currently amended): The liquid crystal display as claimed in claim 1, wherein the second shielding layer overlaps the periphery of the pixel electrode. ~~first shielding layer partially overlaps the periphery of the pixel electrode to form an overlapping portion which serves as a complementary capacitor.~~

Claim 30 (withdrawn): The liquid crystal display as claimed in claim 5, wherein a spacing between the first data line and the periphery of the fist shielding layer is smaller than a spacing between the second data line and the periphery of the second shielding layer.

Claim 31 (currently amended): The liquid crystal display as claimed in claim 1, wherein the first shielding layer partially overlaps the switching element. ~~A liquid crystal display including a plurality of pixel areas, each pixel area comprising:~~

~~a pixel area defined by a first gate line, a second gate line, a first data line, and a second data line, the first or the second data line comprising an extension portion;~~

~~a pixel electrode formed overlying the pixel area;~~

~~a thin film transistor electrically connected to the pixel electrode and positioned on the first or the second gate line, comprising a source electrode and a drain electrode, the source electrode being electrically connected to the extension portion; and~~

~~a first shielding layer directly connected to the first gate line, wherein the first shielding layer is parallel to the first data line and adjacent to the first data line, and overlaps across the extension portion.~~